


FACULTY PROFILE

Name	Mr. PANKAJ DEVIDASRAO POLADE	
Designation	LECTURER IN ELECTRONICS	
Date of Joining	09-09-2009	
Mobile No.	9158459412	
E-Mail ID	Pankajpolade@gmail.com	
Address	GOVERNMENT POLYTECHNIC, RATNAGIRI	

Qualification	Degree	B.E/ BTech	M E/MTech
	Specialization	ELECTRONICS & TELECOMMUNICATION ENGINEERING	
	Institution Name	MSS COLLEGE OF ENGINEERING AND TECHNOLOGY, JALNA	
	University/Board	DR.BABASAHEB AMBEDKAR MARATHWADA UNIVERSITY	
	Percentage	67.00%	
	Year of Passing	2009	

Work Experience		Duration (Years)
Industrial Experience	NIL	
Teaching Experience	GOVERNMENT POLYTECHNIC, RATNAGIRI	11 (till date)
Subjects Taught	Control Systems and PLC, Mechatronics, Microcontroller, Embedded Systems , Elements of Electronics, Digital Techniques, C-PROGRAMING, Fundamentals of Mechatronics, WPX	

Attended Workshop/ Trainings:

Sr. No	Topic	Organizer	Duration	Remarks
1	DTE sponsored One week short term training program on 'Stepping stone to startup India'	Directorate of technical education, Maharashtra state, Mumbai (DTE)	16.12.2019 to 20.12.2019	(One week)
2	AICTE recognized faculty development program on 'Image and Embedded Processing'	NITTTR, Chandigarh.	11.05.20 to 15.05.20	(One week)
3	AICTE recognized faculty development program on 'Technological Interventions using Wireless Communication "	NITTTR, Chandigarh.	18.05.20 to 22.05.20	(One week)

Publications & Research Papers:

Book published on "Control System and PLC" through Vision Publication, Pune.

Membership & Social Activities: